



CONFIDENTIAL
INVENTION REPORT

Infineon
Technologies
Group and partner
companies

Infineon
technologies

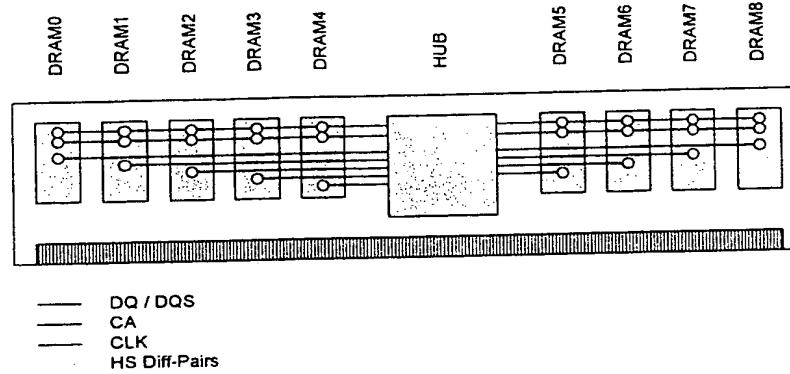


Figure 1: DIMM reference model

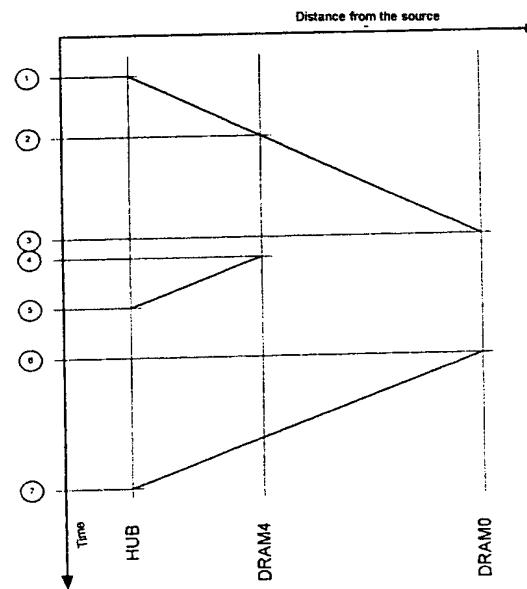


Figure 2: Signal propagation on a DIMM

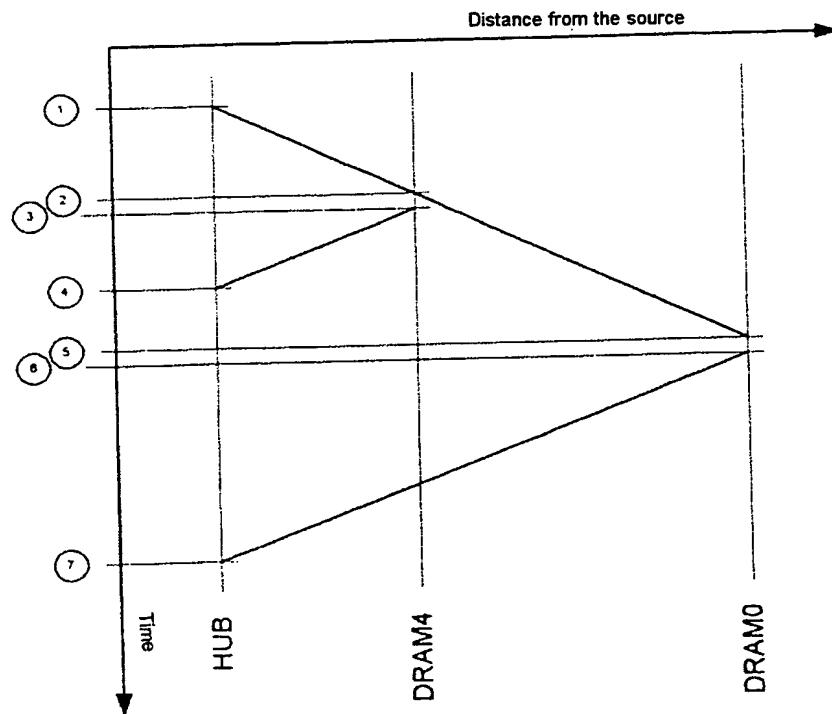


Figure 3: Timing diagram for delay time compensation

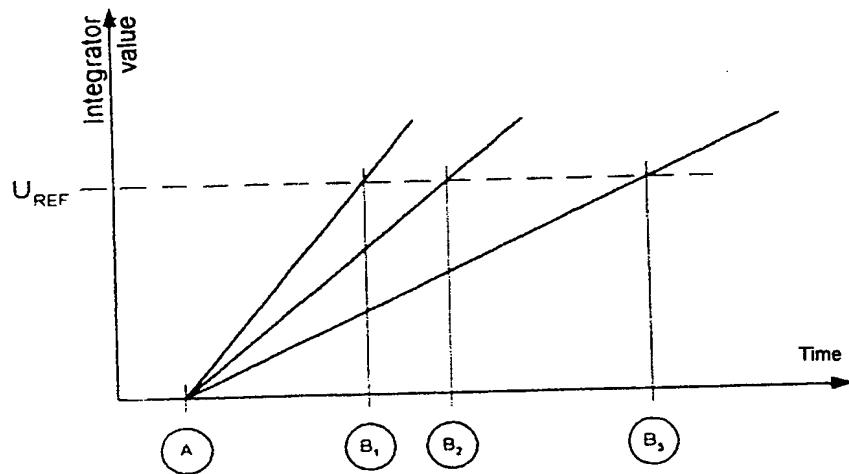


Figure 4: Characteristic diagram of controllable integrators

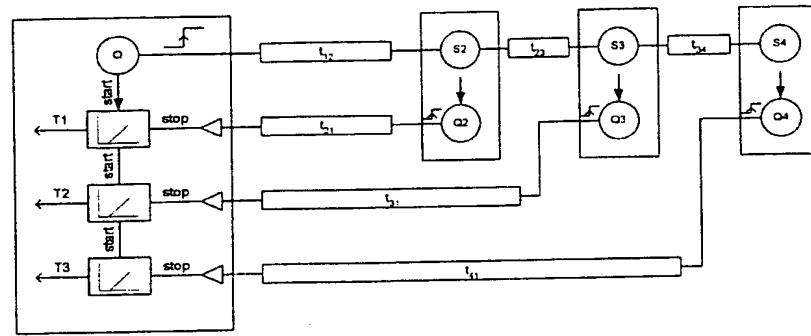


Figure 5: Illustration of the source and sinks involved

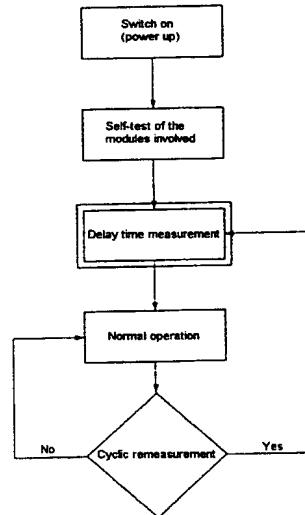


Figure 6: Flowchart for delay time compensation